TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC74HCT157AP,TC74HCT157AF

### **Quad 2-Channel Multiplexer**

The TC74HCT157A is a high speed CMOS 2-CHANNEL MULTIPLEXERs fabricated with silicon gate  $\rm C^2MOS$  technology.

It achieves the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

This device may be used as a level converter for interfacing TTL or NMOS to High Speed CMOS. The inputs are compatible with TTL, NMOS and CMOS output voltage levels.

When  $\overline{STROBE}$  is held high, selection of data is inhibited and all the outputs become low.

The SELECT decoding determines whether the A or B inputs get transferred to their corresponding Y outputs.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

### **Features**

- High speed:  $t_{pd} = 21 \text{ ns (typ.)}$  at  $V_{CC} = 5 \text{ V}$
- Low power dissipation:  $I_{CC} = 4 \mu A \text{ (max)}$  at  $T_a = 25 \text{°C}$
- Compatible with TTL outputs: VIH = 2.0 V (min)

 $V_{IL} = 0.8 \text{ V (max)}$ 

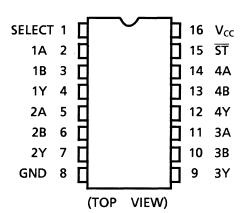
- Wide interfacing ability: LSTTL, NMOS, CMOS
- Output drive capability: 10 LSTTL loads
- Symmetrical output impedance: | I<sub>OH</sub> | = I<sub>OL</sub> = 4 mA (min)
- Balanced propagation delays:  $t_{pLH} \simeq t_{pHL}$
- Pin and function compatible with 74LS157

# DIP16-P-300-2.54A TC74HCT157AF SOP16-P-300-1.27A

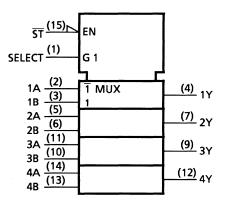
Weight

DIP16-P-300-2.54A : 1.00 g (typ.) SOP16-P-300-1.27A : 0.18 g (typ.)

# **Pin Assignment**



# **IEC Logic Symbol**



## **Truth Table**

	Output					
ST	SELECT	Α	В	Output		
Н	Х	Х	Х	L		
L	L	L	Х	L		
L	L	Н	Х	Н		
L	Н	Х	L	L		
L	Н	Х	Н	Н		

X: Don't care

# **Absolute Maximum Ratings (Note 1)**

Characteristics	Symbol	Rating	Unit
Supply voltage range	V <sub>CC</sub>	–0.5 to 7	V
DC input voltage	V <sub>IN</sub>	−0.5 to V <sub>CC</sub> + 0.5	V
DC output voltage	V <sub>OUT</sub>	-0.5 to V <sub>CC</sub> + 0.5	V
Input diode current	I <sub>IK</sub>	±20	mA
Output diode current	lok	±20	mA
DC output current	lout	±25	mA
DC V <sub>CC</sub> /ground current	Icc	±50	mA
Power dissipation	PD	500 (DIP) (Note 2)/180 (SOP)	mW
Storage temperature	T <sub>stg</sub>	–65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of Ta = -40 to  $65^{\circ}C$ . From Ta = 65 to  $85^{\circ}C$  a derating factor of -10 mW/°C shall be applied until 300 mW.



# **Operating Ranges (Note)**

Characteristics	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	4.5 to 5.5	٧
Input voltage	V <sub>IN</sub>	0 to V <sub>CC</sub>	٧
Output voltage	V <sub>OUT</sub>	0 to V <sub>CC</sub>	V
Operating temperature	T <sub>opr</sub>	−40 to 85	°C
Input rise and fall time	t <sub>r</sub> , t <sub>f</sub>	0 to 500	ns

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.

## **Electrical Characteristics**

## **DC Characteristics**

Characteristics	Symbol	Test Condition  Vcc (V)		Ta = 25°C			Ta = -40 to 85°C		- Unit	
Characteristics	Symbol				Min	Тур.	Max	Min	Max	Offic
High-level input voltage	V <sub>IH</sub>		_		2.0	_	ı	2.0	_	٧
Low-level input voltage	V <sub>IL</sub>		_			_	0.8	_	0.8	٧
High-level output	V <sub>OH</sub>	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	$I_{OH} = -20 \mu A$	4.5	4.4	4.5	_	4.4	_	V
voltage			$I_{OH} = -4 \text{ mA}$	4.5	4.18	4.31	_	4.13	_	
Low-level output	V <sub>OL</sub>	V <sub>IN</sub>	$I_{OL} = 20 \ \mu A$	4.5		0.0	0.1	_	0.1	V
voltage		= V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OL</sub> = 4 mA	4.5	_	0.17	0.26	_	0.33	V
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = V <sub>CC</sub> or GND		5.5	_	_	±0.1	_	±1.0	μΑ
Quiescent supply current	Icc	$V_{IN} = V_C$	<sub>C</sub> or GND	5.5	_	_	4.0	_	40.0	μА
	IC	Per input: $V_{IN} = 0.5 \text{ V or } 2.4 \text{ V}$ Other input: $V_{CC}$ or GND		5.5		_	2.0	_	2.9	mA

# AC Characteristics (C<sub>L</sub> = 15 pF, $V_{CC}$ = 5 V, Ta = 25°C, input: $t_r$ = $t_f$ = 6 ns)

Characteristics	Symbol	mbol Test Condition		Тур.	Max	Unit
Output transition time	t <sub>TLH</sub> t <sub>THL</sub>	_	_	4	8	ns
Propagation delay time (A, B-Y)	t <sub>pLH</sub>	_	_	19	30	ns
Propagation delay time ( STROBE -Y)	t <sub>pLH</sub>	_	_	19	30	ns
Propagation delay time (SELECT-Y)	t <sub>pLH</sub> t <sub>pHL</sub>	_	_	21	32	ns



AC Characteristics ( $C_L = 50 \text{ pF}$ , input:  $t_r = t_f = 6 \text{ ns}$ )

Characteristics	Symbol	Test Condition	n Ta = 25°C			Ta = -40 85°C		Unit	
Characteristics	Cymbol		V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Offic
Output transition time	t <sub>TLH</sub>		4.5	_	8	15	_	19	ns
Output transition time	t <sub>THL</sub>	_	5.5	_	7	14	_	18	115
Propagation delay time	t <sub>pLH</sub>	_	4.5	_	23	35	_	44	ns
(A, B-Y)	$t_{pHL}$		5.5	_	20	32	_	40	
Propagation delay time	t <sub>pLH</sub>	_	4.5	_	23	35	_	44	ns
(STROBE -Y)	$t_{pHL}$		5.5	_	20	32	_	40	
Propagation delay time	t <sub>pLH</sub>	_	4.5	_	25	37	_	46	ns
(SELECT-Y)	t <sub>pHL</sub>		5.5	_	21	34		42	
Input capacitance	C <sub>IN</sub>	_	•	_	5	10	_	10	pF
Power dissipation capacitance	C <sub>PD</sub> (Note)	_		_	59	_	_	_	pF

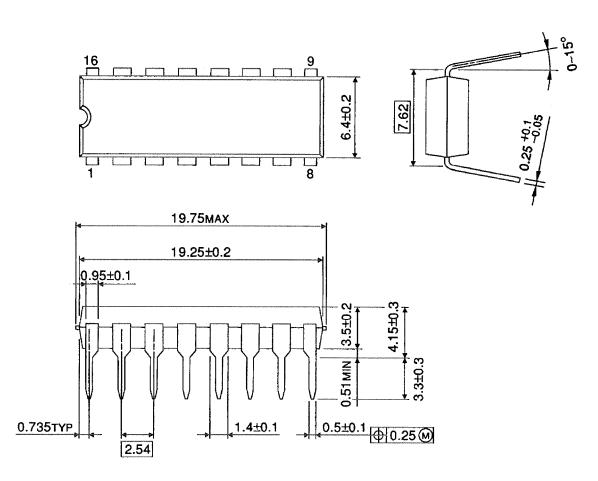
Note: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

$$I_{CC}$$
 (opr) =  $C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/4$  (per bit)

# **Package Dimensions**

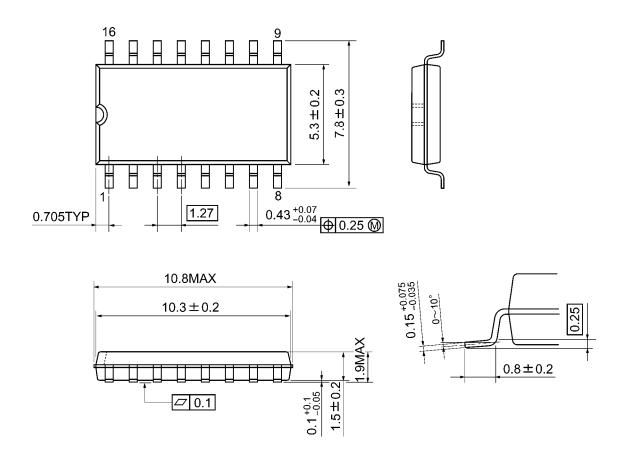
DIP16-P-300-2.54A Unit: mm



Weight: 1.00 g (typ.)

# **Package Dimensions**

SOP16-P-300-1.27A Unit: mm



Weight: 0.18 g (typ.)

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